
Editorial

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Biographical notes: P. Karthigai Kumar received his PhD in Information and Communication Engineering under Anna University, India in 2011, focusing on FPGA and ASIC implementation of Media Security Processor. He is a senior member of Association of Computer Electronics and Electrical Engineers (ACEEE), member of International Association of Engineers (MIAENG) and member of International Association of Computer sciences and Information Technology (MIACSIT). He has published 70 papers in an international journal. He received his IETE KS Krishnan Award for the best system oriented research paper in 2010. His research interest includes FPGA implementation of media security algorithm.

Anand Paul he is currently working in the School of Computer Science and Engineering, Kyungpook National University, South Korea as an Assistant Professor. He got his PhD in the Electrical Engineering at National Cheng Kung University, Taiwan, ROC in 2010. His research interests include algorithm and architecture reconfigurable embedded computing. He is a delegate representing South Korea for M2M focus group and for MPEG. He has been awarded with the Outstanding International Student Scholarship in 2004–2010 and won the Best Paper Award in National Computer Symposium, Taipei, Taiwan in 2009.

Joy Iong-Zong Chen was born in Taiwan. He received his BSc in Electronics Engineering from the National Taiwan Technical University, Taipei, Taiwan, and MSc in Electrical Engineering from the Da-Yeh University, Chung-Hua, Taiwan, in 1985 and 1995, respectively, and PhD in Electrical Engineering from National Defense University, Tao-Yuan, Taiwan, in 2001. He is currently a Full Significant Professor of Department of Communication Engineering Da-Yeh University at Chang-Hua Taiwan. Prior to joining the Da-Yeh University, he worked at the Control Data Company (Taiwan) as a Technical Manager since September 1985 to September 1996. He has published about 40 international journal papers form scholar until now, and acts as Guest Editor for several famous international journals. His research interests include wireless communications, spread spectrum technical, OFDM systems, and wireless sensor networks, MEMS development.

The internet of things concept has progressively expanded to cover applications in many daily life frameworks. It now encompasses several technologies beyond intelligent technologies, while the prevalent internet continuously advances our expectations of permanent interaction with the physical environment. In addition, performance evaluation is important, with diverse approaches ranging from mathematical analysis to statistical simulations and new technology analyses.

This special issue covers theoretical and academic aspects of the internet of multimedia things and multimedia security, as well as industrial and commercial applications developed in this area. We accepted five papers which are good in technical findings. The first paper, "A low area VLSI implementation of extended tiny encryption algorithm using Lorenz chaotic system," is co-authored by A. Shailaja and K. Gorappa Ningappa. The second paper, "A novel energy efficient routing algorithm for MPLS-MANET using fuzzy logic controller," is co-authored by B.J. Ambika and M.K. Banga. The next paper, "Video watermarking using neural networks," is co-authored by S. Bhargavi Latha, D. Venkata Reddy, and A. Damodaram. The fourth paper by G.L. Anil and J.L. Mazher Iqbal, discusses "Secure energy efficient network priority routing protocol for effective data collection and key management in dynamic WSNs." In the fifth paper, Y. Jayasimha and R.V. Siva Reddy propose "A facial expression recognition model using hybrid feature selection and support vector machines."

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