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## Preface

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**Biographical notes:** Angsuman Sarkar is presently serving as an Associate Professor of Electronics and Communication Engineering from the Kalyani Government Engineering College, West Bengal. He had earlier served as a Lecturer at the Jalpaiguri Government Engineering College, West Bengal of the ECE Department for nine years. He received his MTech in VLSI and Microelectronics from the Jadavpur University. He completed his PhD from the Jadavpur University, in 2013. His current research interests span around the study of short channel effects of sub 100 nm MOSFETs and nanodevice modelling. He is a life member of the Indian Society for Technical Education (ISTE) and Institution of Engineers (India), senior member of IEEE and current Chair of Electron Device Society, Kolkata Section. He has authored many books and a number of research papers in national and international refereed journals and conferences.

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This special issue of *International Journal on Nanoparticles* on ‘Nanotechnology and high-speed electronic systems’ is devoted to the design, modelling, fabrication and characterisation of nano-dimensional high speed devices, and related technologies and applications, was presented. This special issue aims to provide an overview of latest significant advances in nanotechnology and high speed device architectures and material technologies. This special issue consists of seven articles carefully selected from the papers presented at the Devices for Integrated Circuits (DevIC 2017) Conference held on 23–24 March 2017 at the Kalyani Government Engineering College, Kalyani, India.

We thank all of the authors from around the world for their important contributions to this special issue. We also thank the reviewers for their important advice, and we owe special thanks to the Editorial Office of *International Journal of Nanoparticles* for their sincere and patient work. We hope that you will find this special issue interesting and that you will consider participation in the future ‘DevIC’ conferences.