Editorial

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Biographical notes: Sathasivam Sivanantham received his BE in Electronics and Communication Engineering from the University of Madras, India, MTech in VLSI Design from SASTRA University, India and PhD in Electrical Engineering from the Vellore Institute of Technology (VIT), India, in 1997, 2002 and 2014, respectively. He is currently an Associate Professor at the School of Electronics Engineering and Assistant Dean of Academic Research with the VIT, Vellore, India. His areas of research interests include the design for testability, reconfigurable architectures, and low power VLSI design. He is a senior member of IEEE and published several papers in IEEE conferences and reputed journals.

A. Nirmala Grace received her PhD in Chemistry/Nanotechnology from the University of Madras, India. She worked as a Postdoctoral/Senior Researcher Fellow at the Korea Institute of Energy Research, South Korea on renewable energy. Her current research interests is on various niche areas of nanotechnology like hybrid nanomaterials, synthesis, applications in energy sector, renewable energy, energy storage, fuel cells, batteries, photoelectrocatalysis, H2 production; environmental, photocatalysis and

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adsorbents for organic and a few others. She has guided more than ten PhD scholars and 40 Masters project students. She has completed two sponsored research projects from L-RAMP and DST, Government of India with three ongoing research projects from DST and DRDO, Government of India. She has more than 80 international peer reviewed publications, has authored two book chapters and many conference publications.

Partha Sarathi Mallick served as the Dean of School of Electrical Engineering (2011–2015). He worked as the Technical Head at IAAB Electronics, a Swedish Industry in Bangladesh. He is an enlisted technical innovator of DSIR, Government of India in 2007. He completed his PhD from Jadavpur University, Kolkata in 2001 and received the prestigious Jawaharlal Nehru Fellowship. He is a senior member of IEEE, past Chapter Chair and present Chapter Adviser of IEEE-EDS VIT of Region 10 Asia Pacific. He has published more than 100 papers in reputed journals and conferences and authored a book on Matlab and Simulink.

Thinagaran Perumal is the recipient of 2014 Early Career Award from IEEE Consumer Electronics Society for his pioneering contribution in the field of consumer electronics. He completed his PhD in the area of smart technology and robotics. He is currently a Senior Lecturer at the Department of Computer Science, Universiti Putra Malaysia. He is currently appointed as Head of Cyber-Physical Systems in the university and also been elected as Chair of IEEE Consumer Electronics Society Malaysia Chapter. His research interests are towards interoperability aspects of smart homes and IoT, wearable computing, and cyber-physical systems.

The 'World Summit on Advances in Science, Engineering, and Technology (Cambridge Summit 2018)' held during 4–6 January 2018 at Cambridge University, UK organised by the Vellore Institute of Technology, Tamil Nadu, India. It provided a platform for defining, introducing, and presenting utterly novel methodologies for responsibly addressing the pressing contemporary science, engineering and technology (SET) problems. At Cambridge Summit 2018, there are more than 150 researchers presented their findings, gave practical recommendations and good solutions to the current and near-future SET challenges. This summit actively promotes finding matches between solution seekers and solution providers. This special issue on *Advances in Engineering Systems and Product Technology* is the outcome selected papers from this summit. These articles are peer-reviewed by the experts in their field and recommended for publication by the guest editors of this issue.