
Preface

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Biographical notes: Sunita Chandrasekaran is an Assistant Professor at the Computer and Information Sciences Department, University of Delaware. Her research interests include exploring the suitability of high-level programming models and runtime systems for HPC, and migrating scientific applications to heterogeneous computing systems. She was a Postdoctoral Fellow at the UH and holds a PhD from the NTU, Singapore. She received the 2016 IEEE-CS TCHPC Award for Excellence for Early Career Researchers in HPC. She has served on the PCs of conferences, including SC, ISC, and ICPP, and co-chaired parallel programming workshops co-located with SC, ISC, IPDPS and SIAM.

Guido Juckeland founded the Computational Science Group at the Helmholtz-Zentrum Dresden-Rossendorf (HZDR), Germany. He is responsible for designing and implementing end-to-end research IT-workflows together with scientists and IT experts at HZDR. His research focuses on better usability and programmability for accelerators and application performance monitoring along with optimisation. He is the Vice Chair of the SPEC High Performance Group (HPG), Secretary of OpenACC, and also contributes to the OpenMP tools working group. He earned his PhD in Computer Science from the Technische Universität Dresden, Germany, for his work on trace-based performance analysis for hardware accelerators.

The Third Workshop on Accelerator Programming using Directives (WACCPD) was co-located with SC 2016 held on November 14, 2016, at Salt Lake City, Utah, USA. The workshop solicited papers on topics including hybrid heterogeneous or many-core programming with accelerator directives with other models (i.e., OpenMP, MPI, OpenSHMEM), scientific libraries interoperability with accelerator directives, programming experience porting applications in any domain, language-based extensions, and modelling and performance analysis tools.

Among the 20 submissions to the WACCPD workshop, we invited authors of seven papers to submit their extended manuscripts to a special issue with *IJHPCN*. The authors were asked to extend their paper by at the least 30% for the special issue by adding newer contents.

These seven manuscripts were peer-reviewed and the review process for the special issue was not double blind, i.e., authors were known to reviewers. Submissions were judged on correctness, originality, technical strength, and significance, quality of presentation, and interest and relevance to the conference scope. All the seven

manuscripts were accepted to the special issue after two rounds of review.

Reviewers

- Samuel Thibault, INRIA, University of Bordeaux, France
- James Beyer, NVIDIA, USA
- Wei Ding, AMD, USA
- Saber Feki, King Abdullah University, Saudi Arabia
- Robert Henschel, Indiana University, USA
- Michael Klemm, Intel, USA
- Eric Stotzer, Texas Instruments, USA
- Amit Amritkar, University of Houston, USA
- Will Sawyer, ETH, Zurich
- Sameer Shende, University of Oregon, USA

- Costas Bekas, IBM, Zurich
- Toni Collis, University of Edinburgh, Scotland
- Adrian Jackson, University of Edinburgh, Scotland
- Henri Jin, NASA, USA
- Andreas Knuepfer, TU Dresden, Germany
- Steven Olivier, Sandia National Laboratory, USA
- Suraj Prabhakaran, TU Darmstadt, Germany
- Bora Ucar, ENS De Lyon, France
- Manisha Gajbe, Intel, USA
- Daniel Tian, PGI, USA